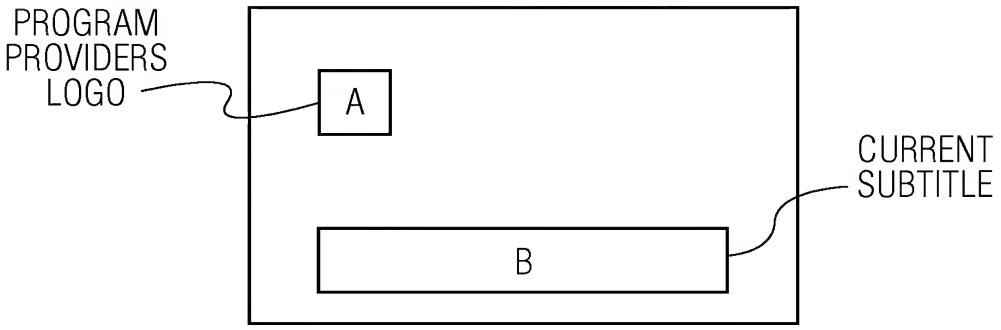

FIG. 1

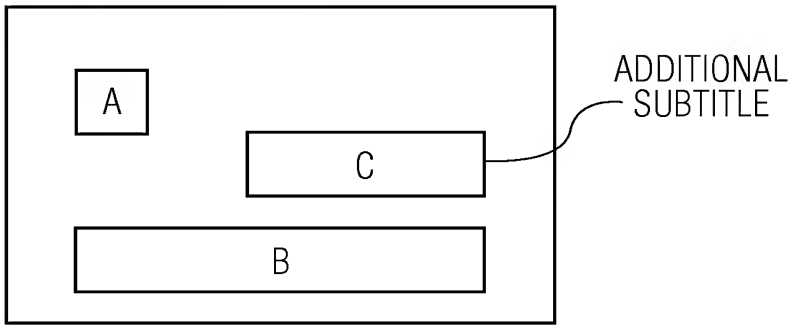

FIG. 2

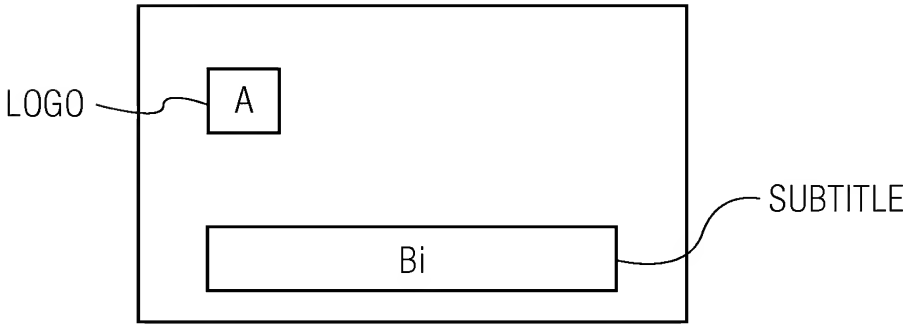

FIG. 3

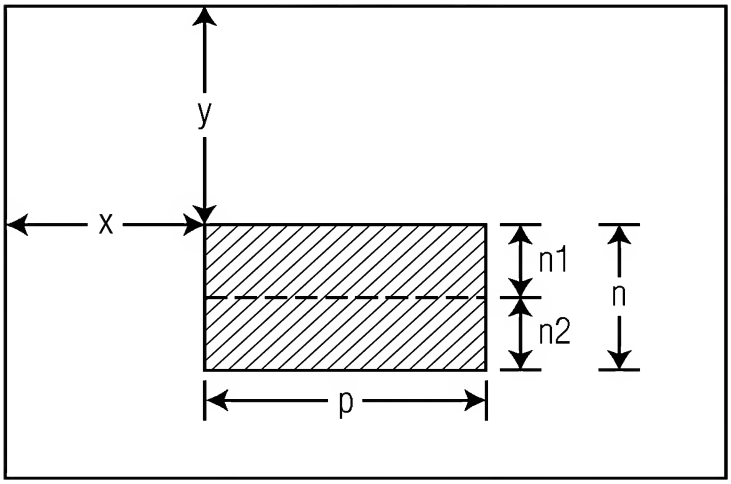
y
x
n1
n2
n
p

FIG. 4

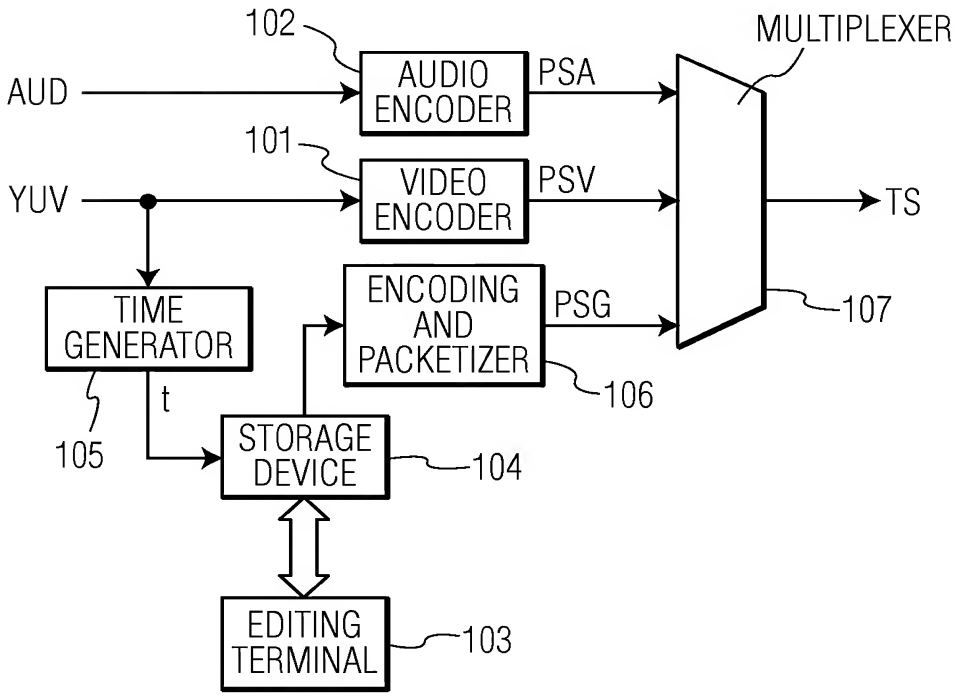
102
AUDIO ENCODER
PSA
MULTIPLEXER
AUD
101
VIDEO ENCODER
PSV
YUV
TS
TIME GENERATOR
ENCODING AND PACKETIZER
PSG
107
106
105
t
STORAGE DEVICE
104
EDITING TERMINAL
103

FIG. 5

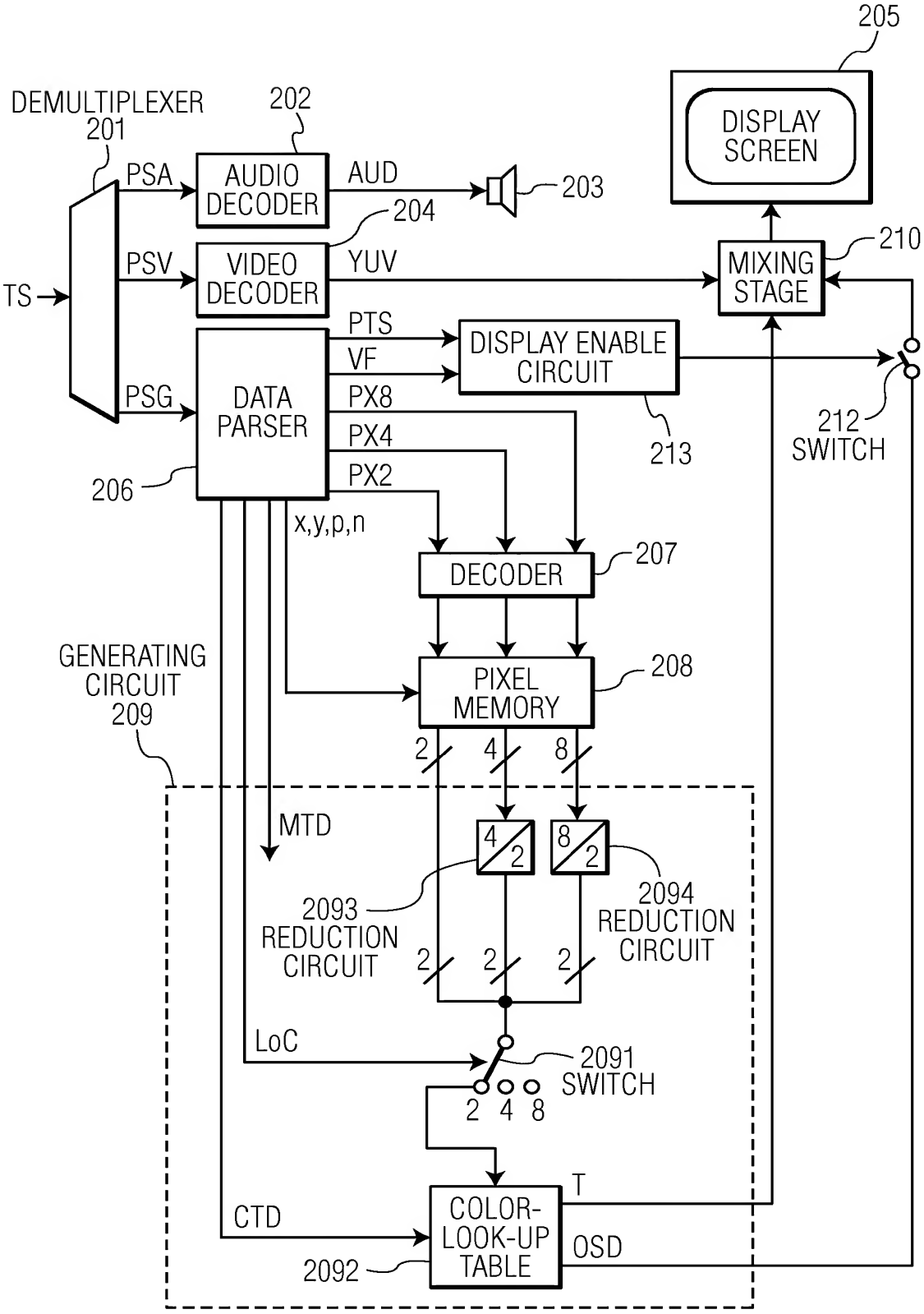
205
DISPLAY SCREEN
DEMULTIPLEXER
201
202
AUDIO DECODER
AUD
203
PSA
204
PSV
VIDEO DECODER
YUV
TS
MIXING STAGE
210
PTS
VF
DISPLAY ENABLE CIRCUIT
PSG
DATA PARSER
PX8
PX4
PX2
213
212 SWITCH
206
x,y,p,n
DECODER
207
GENERATING CIRCUIT 209
PIXEL MEMORY
208
2
4
8
MTD
4/2
8/2
2093 REDUCTION CIRCUIT
2094 REDUCTION CIRCUIT
2
2
2
LoC
2091 SWITCH
2 4 8
T
CTD
COLOR-LOOK-UP TABLE
OSD
2092

FIG. 6

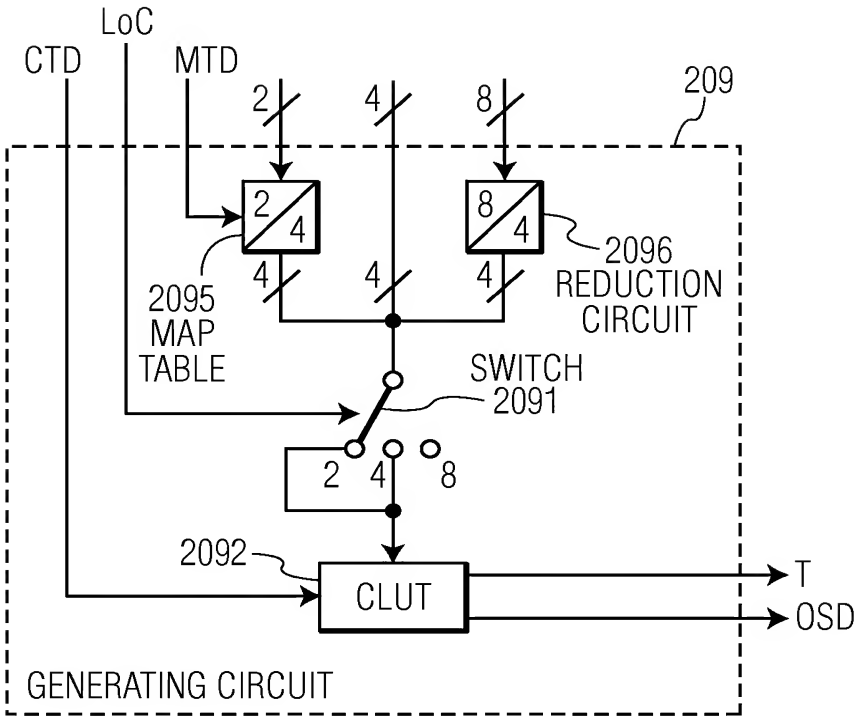
LoC
CTD
MTD
2
4
8
209
2/4
8/4
2096
REDUCTION
CIRCUIT
4
4
4
2095
MAP
TABLE
SWITCH
2091
2
4
8
2092
CLUT
T
OSD
GENERATING CIRCUIT

FIG. 7

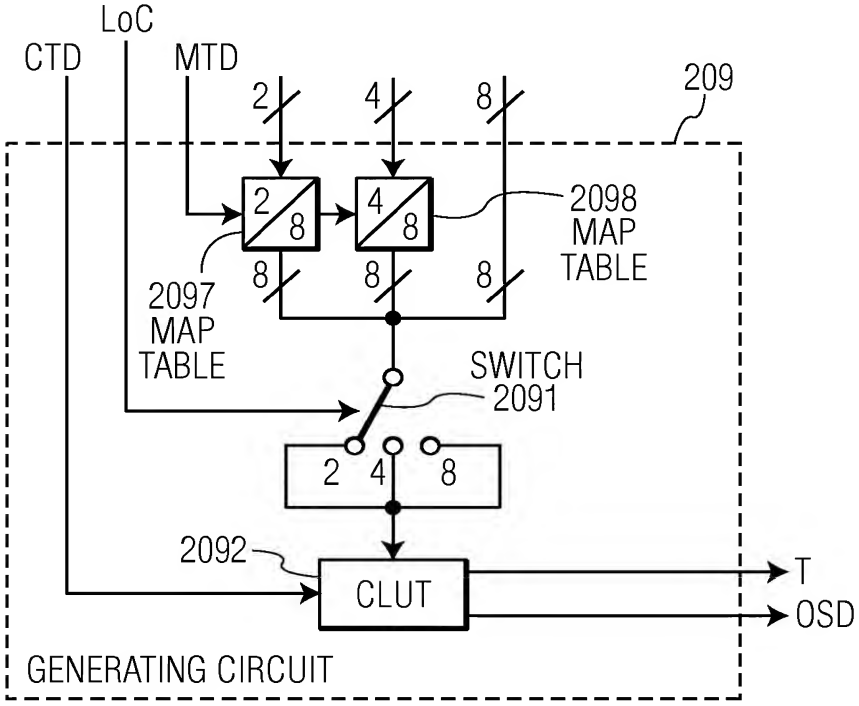
LoC
CTD
MTD
2
4
8
209
2/8
4/8
2098
MAP
TABLE
8
8
8
2097
MAP
TABLE
SWITCH
2091
2
4
8
2092
CLUT
T
OSD
GENERATING CIRCUIT

FIG. 8